

1/15

Fig 1

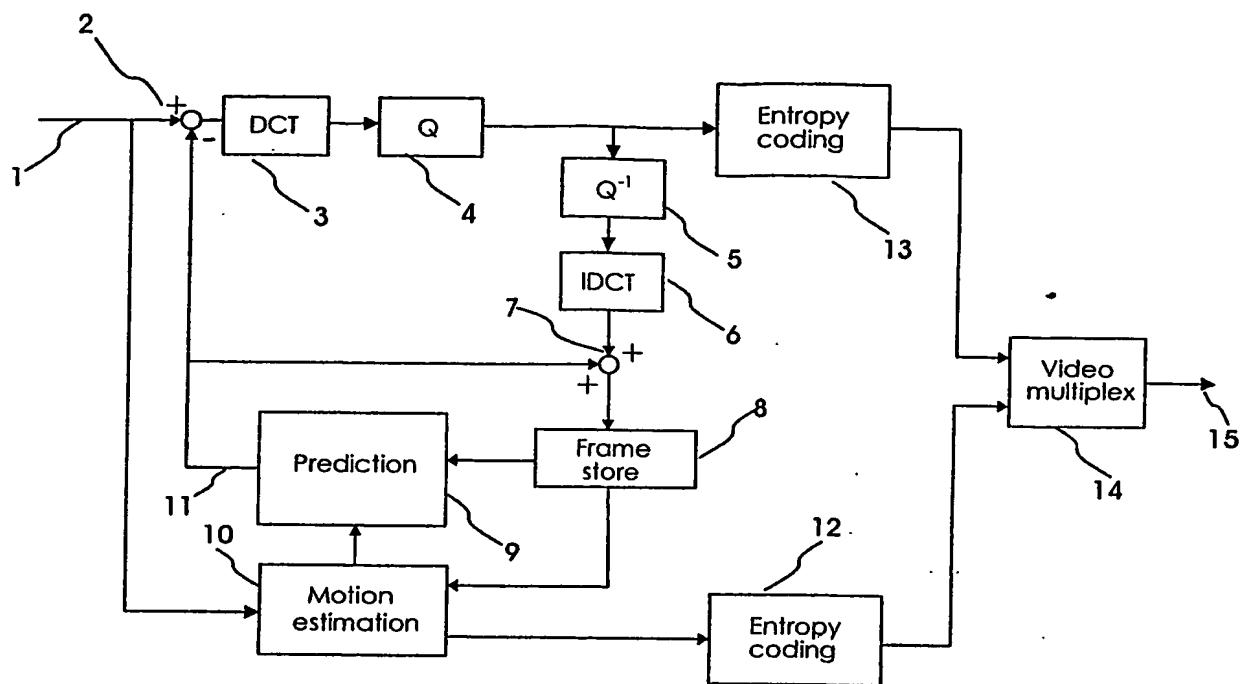
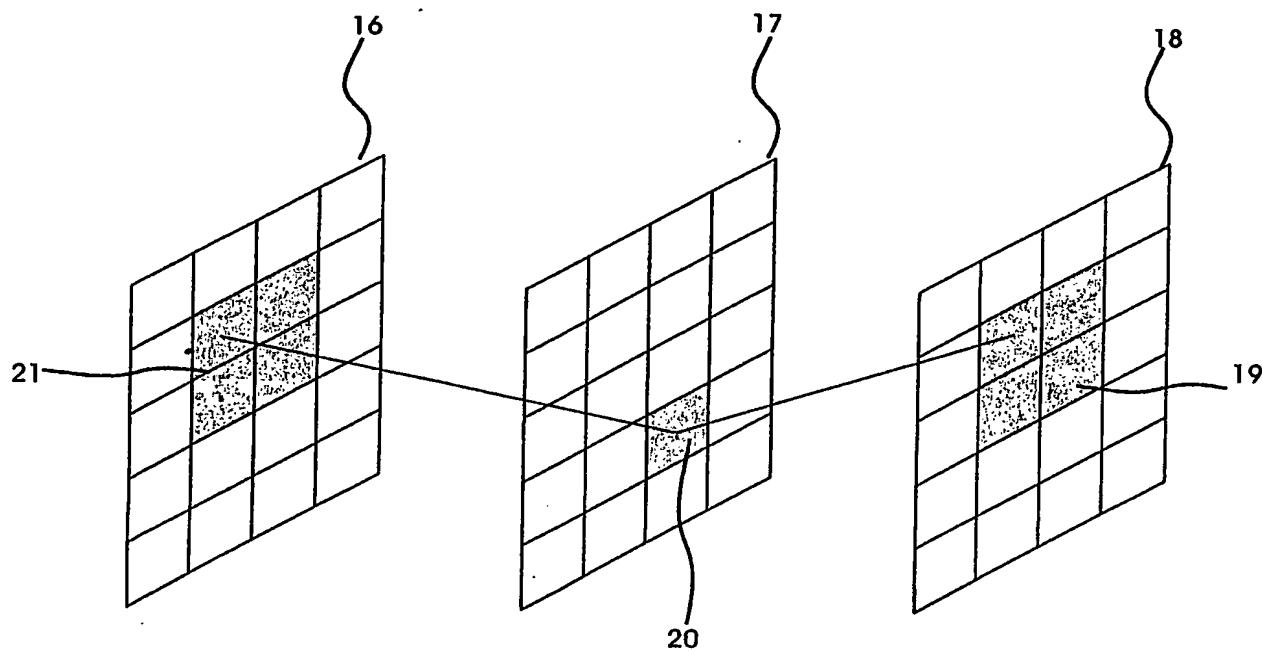


Fig 2



2/15

Fig 3

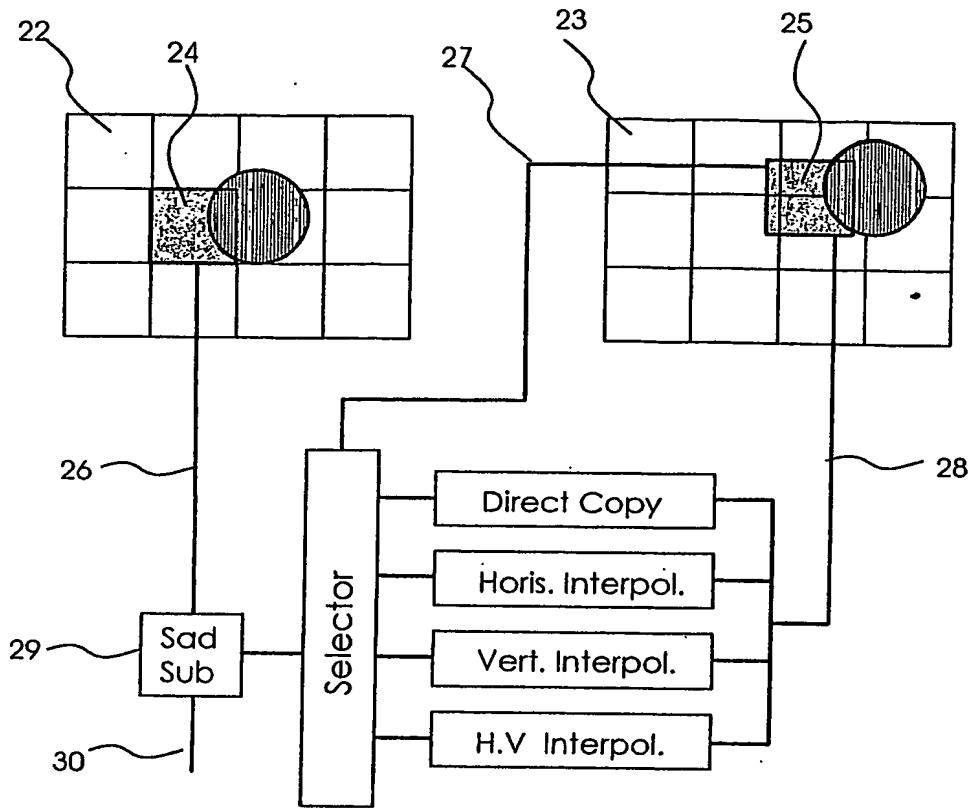
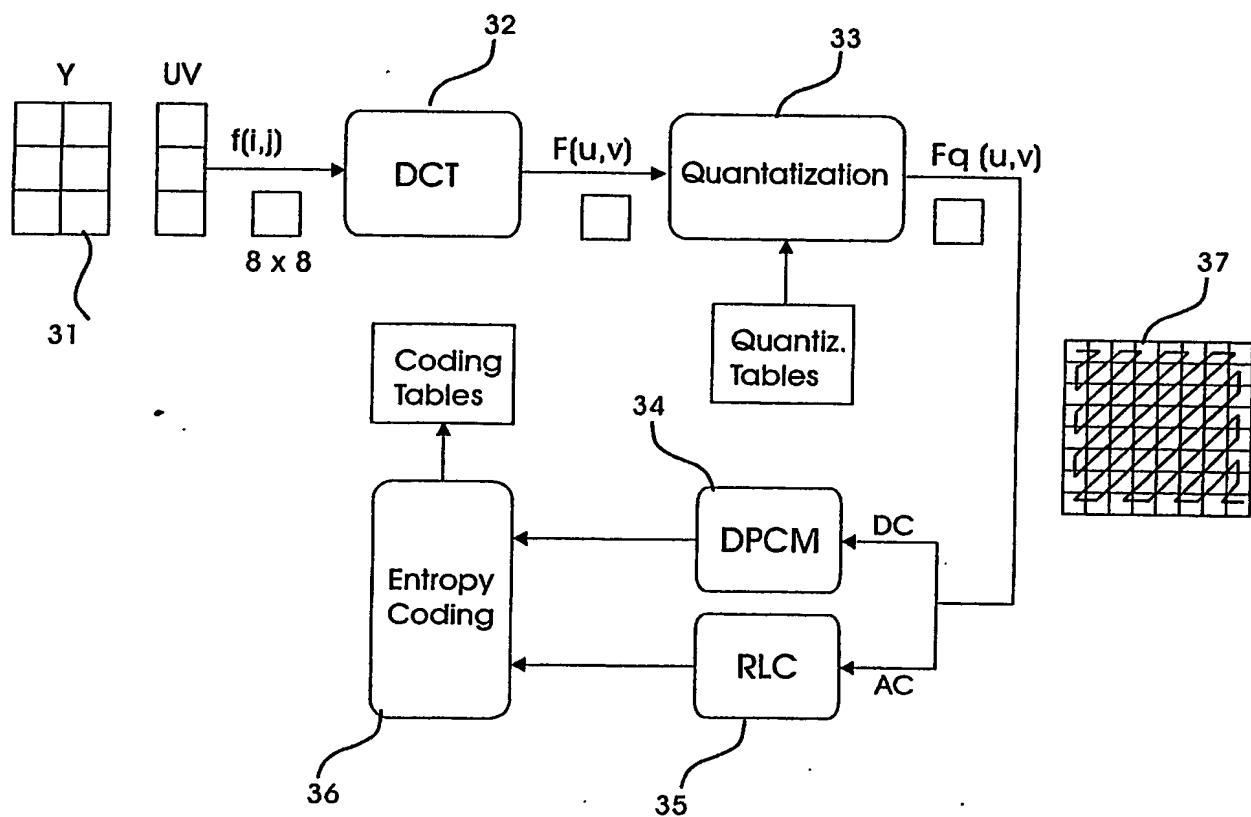


Fig 4



3/15

Fig 5

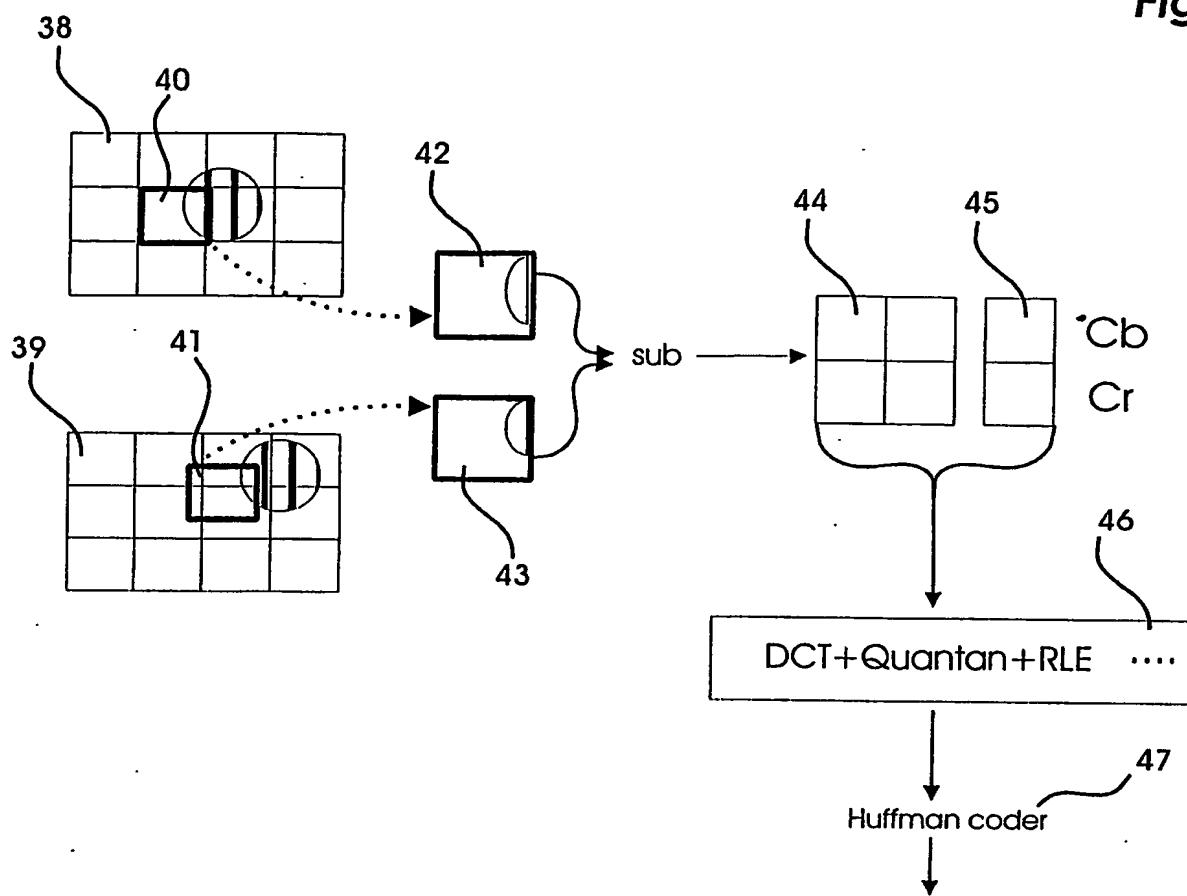
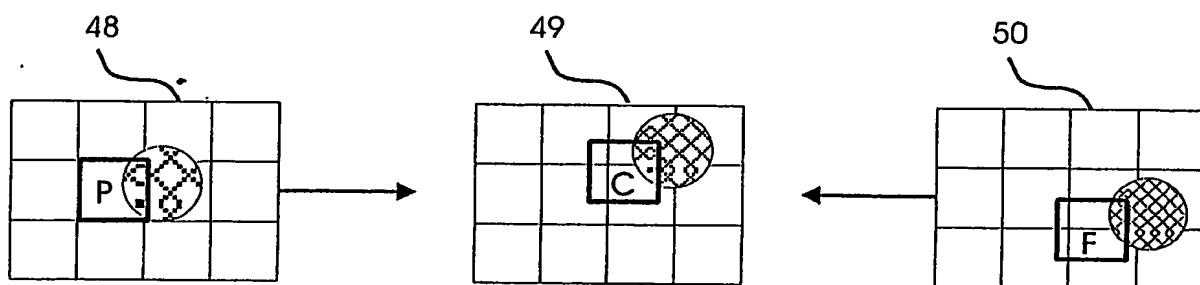


Fig 6



4145

Fig 7

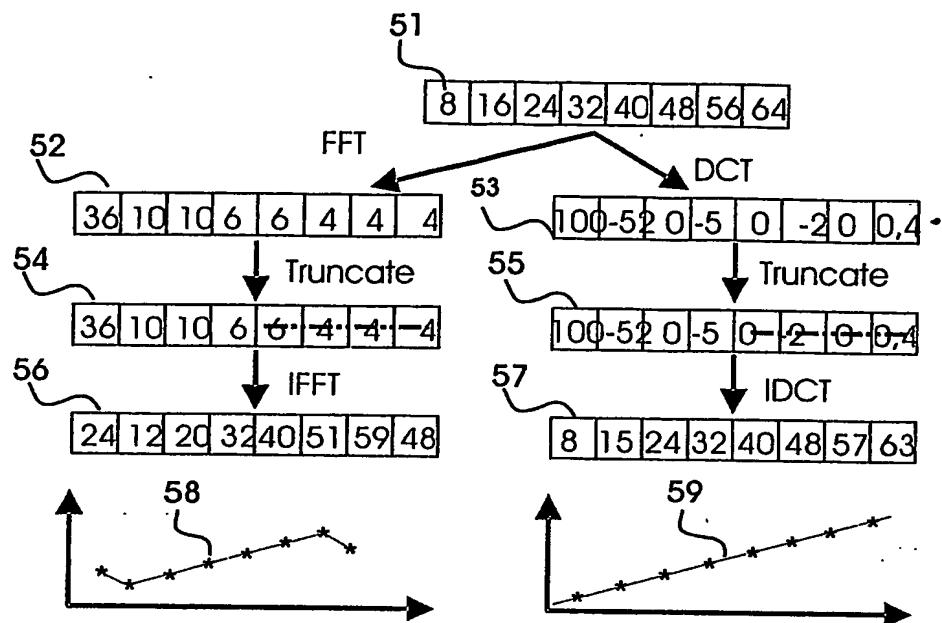
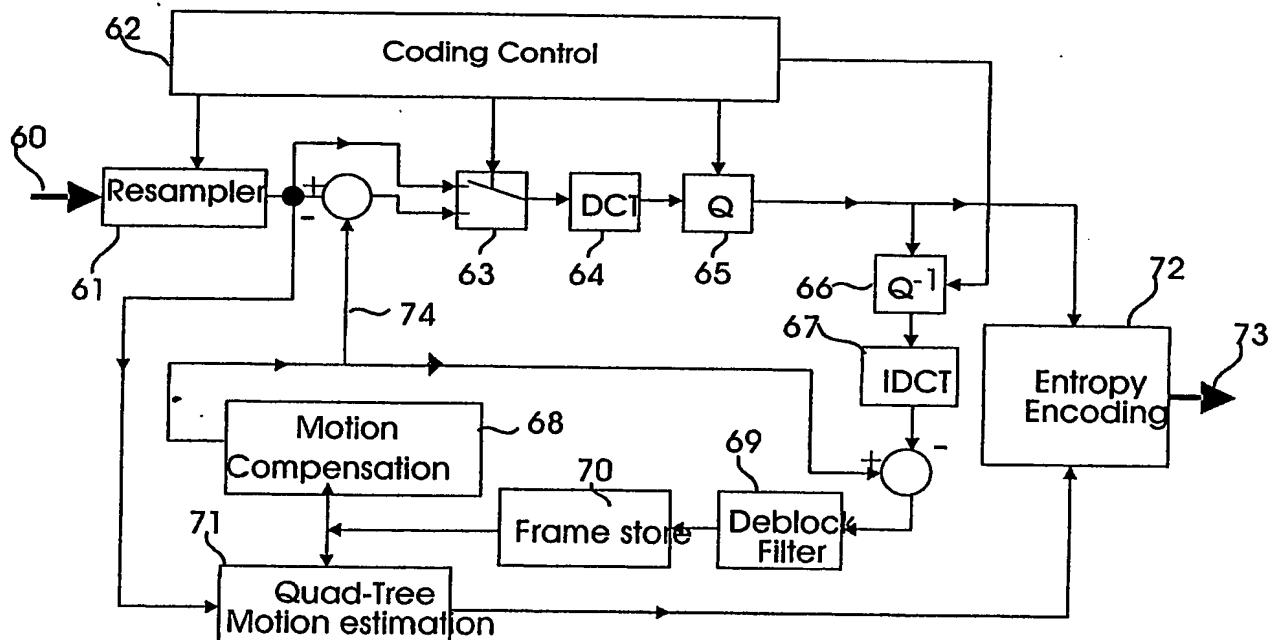


Fig 8



5/15

Fig 9

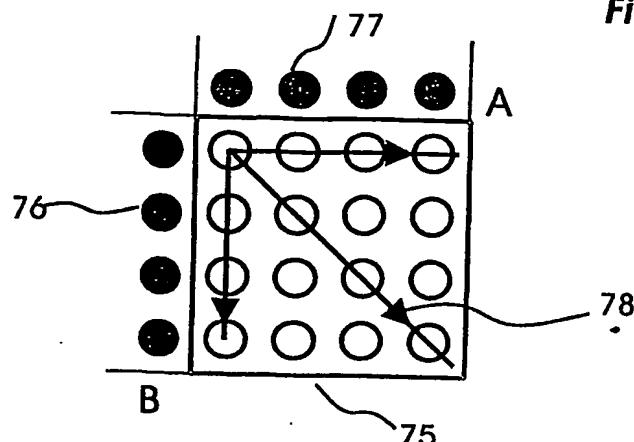


Fig 10

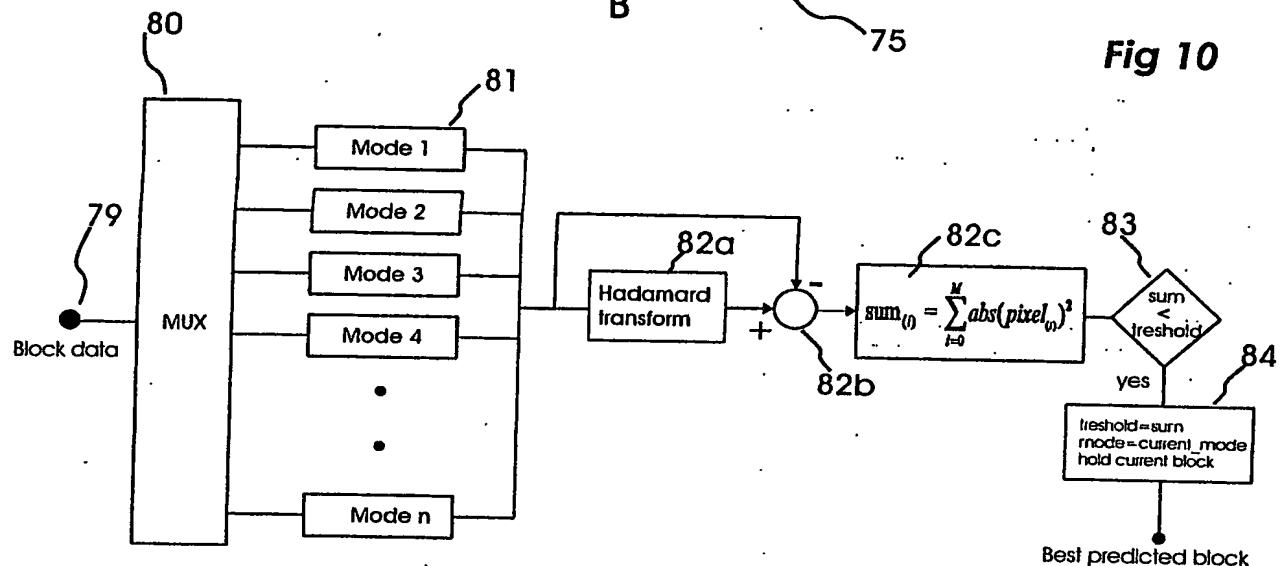
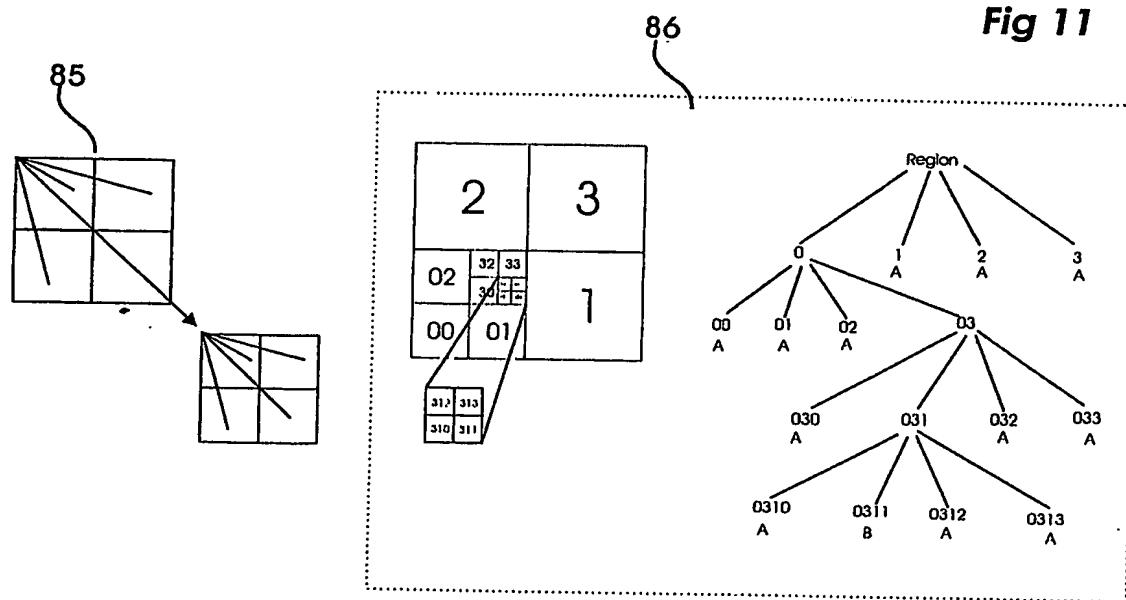


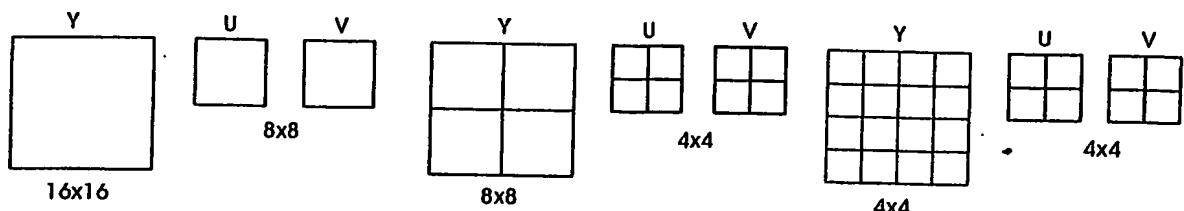
Fig 11



6/15

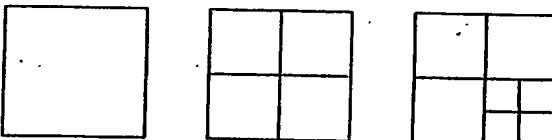
Fig 12

Basic block partition



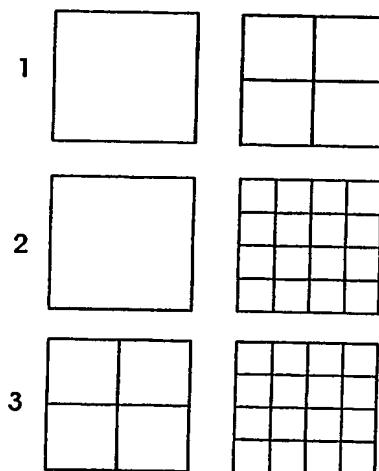
Three block mode

Fig 13



Two block mode

Fig 14



7/15

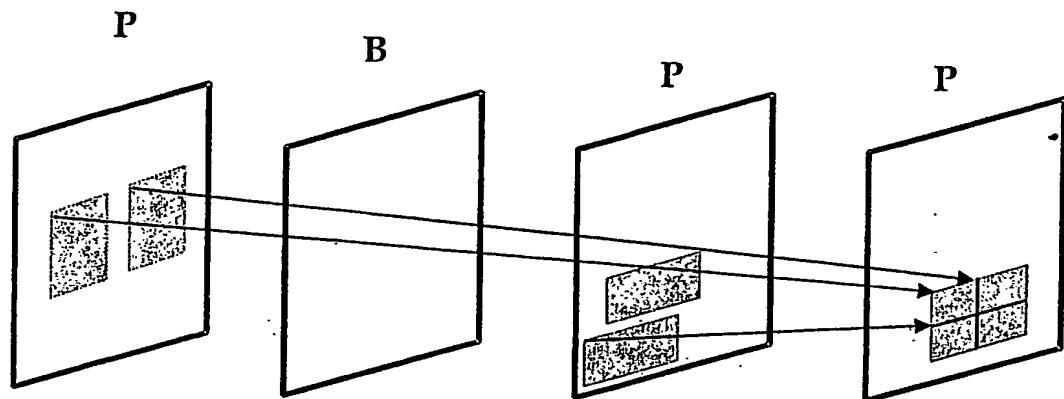


Fig. 15a

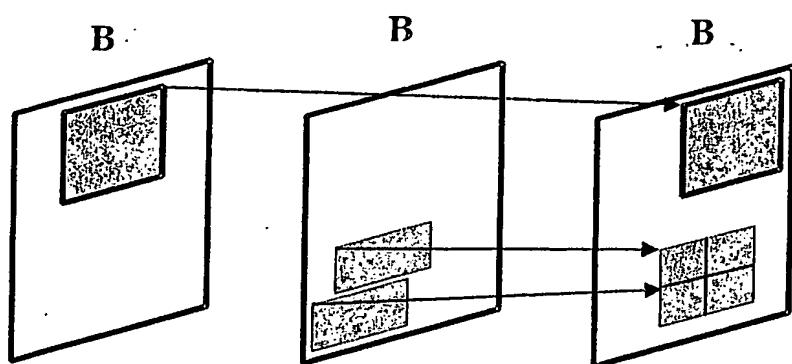


Fig. 15b

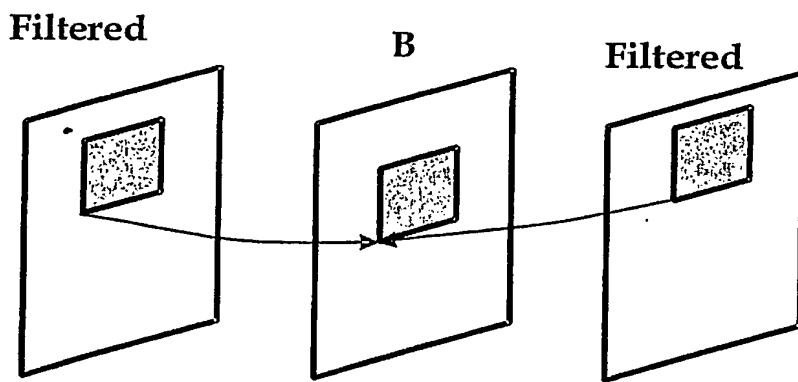
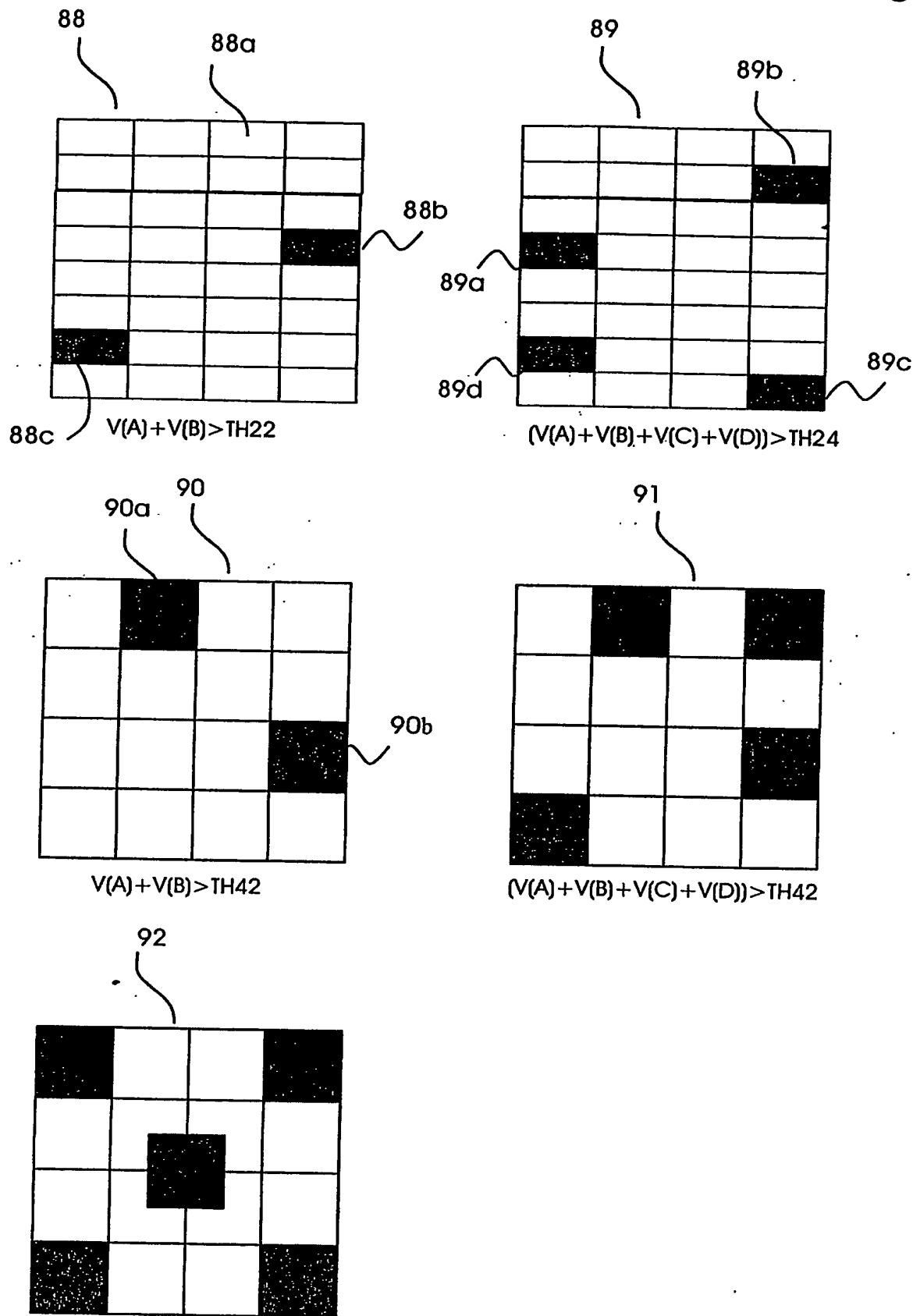


Fig. 15c

8/15

Fig 16



9/15

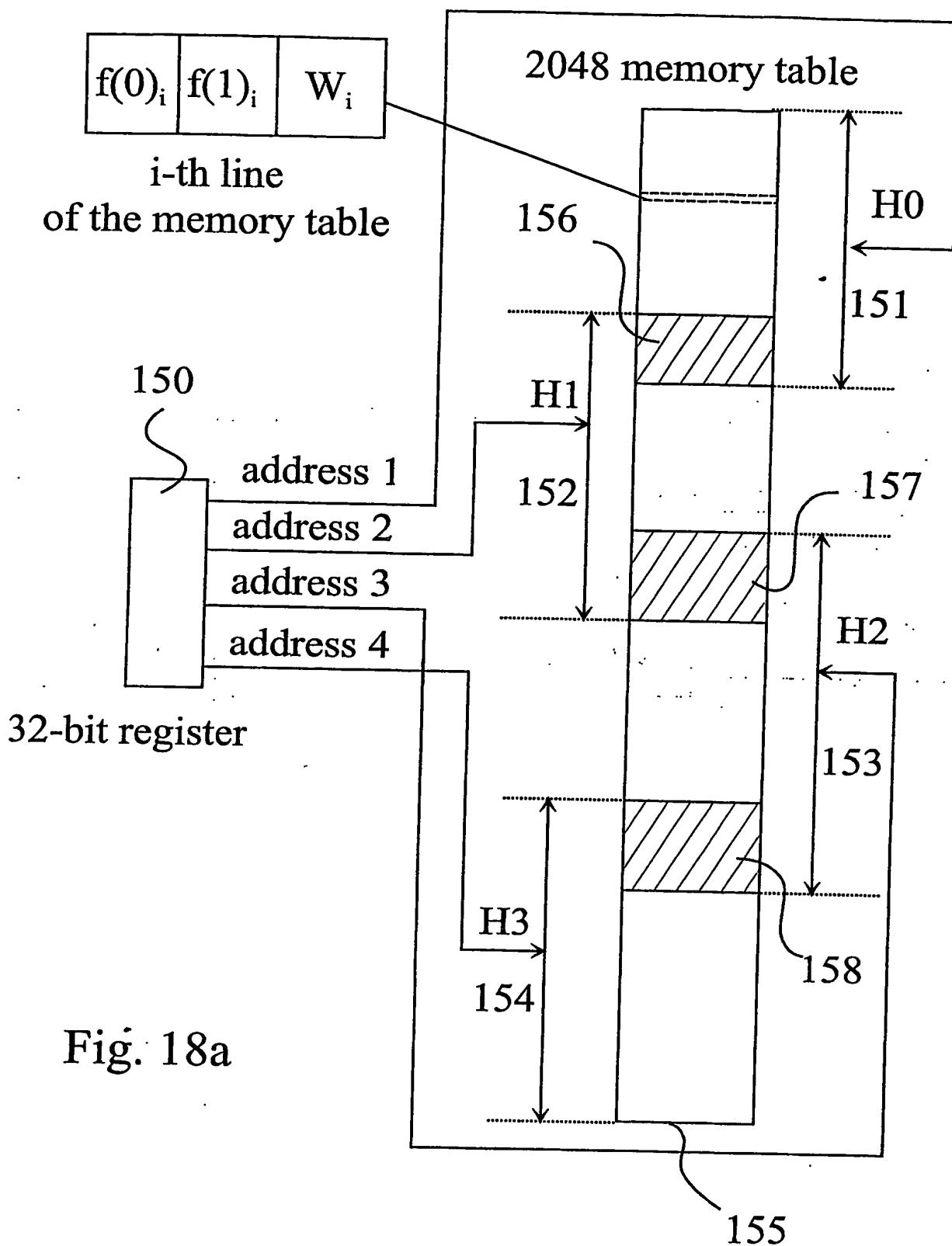
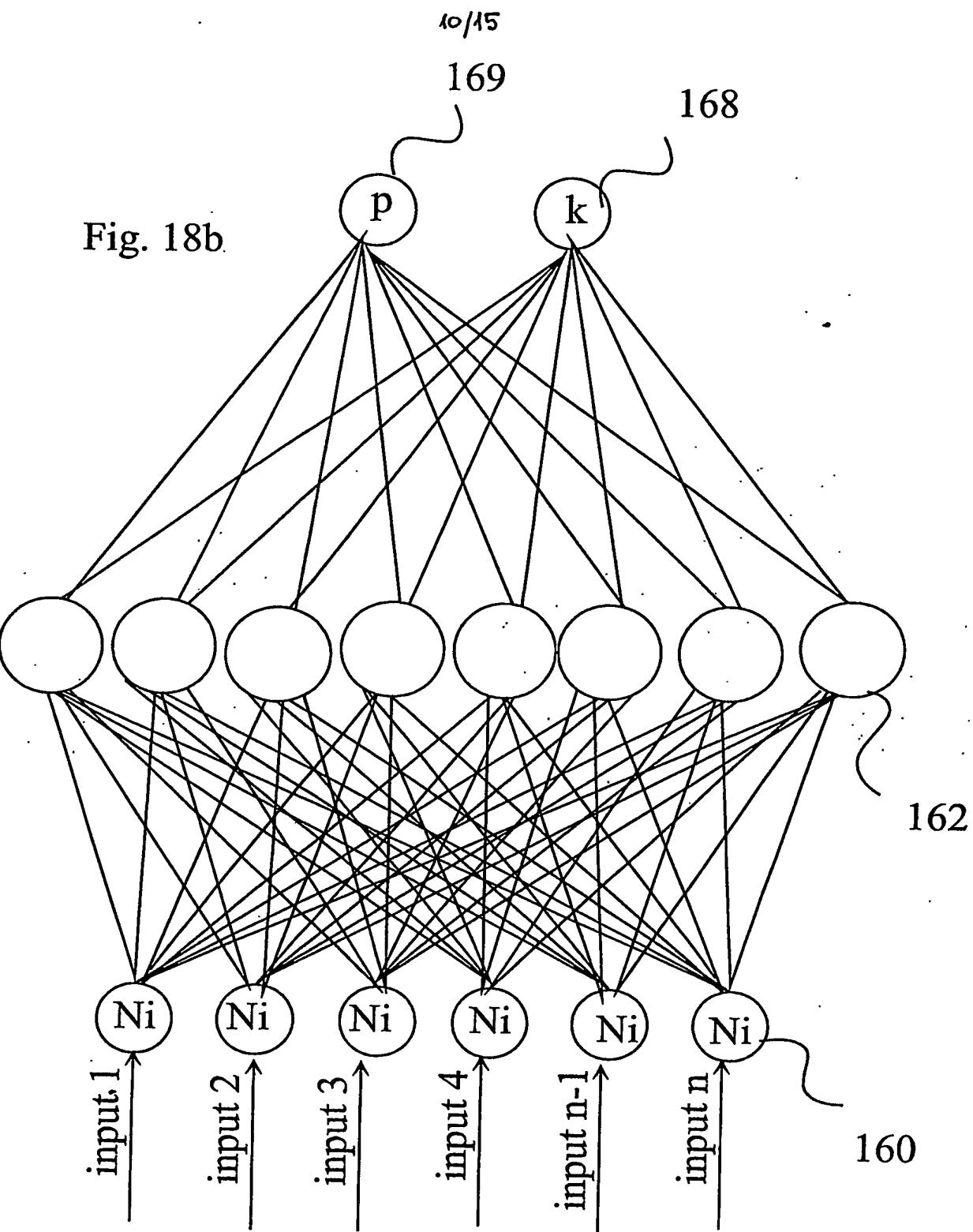


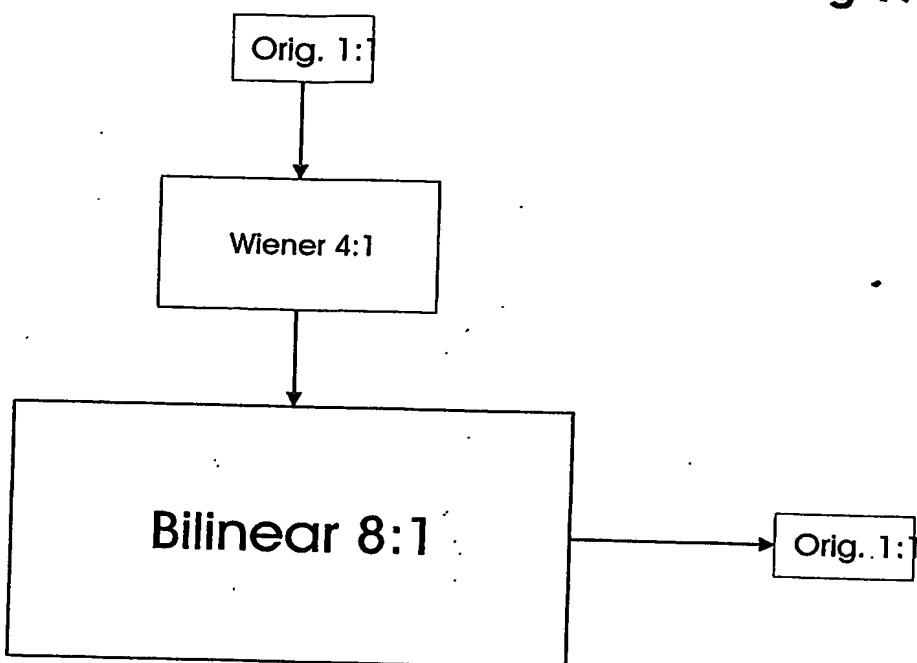
Fig. 18a



11/15

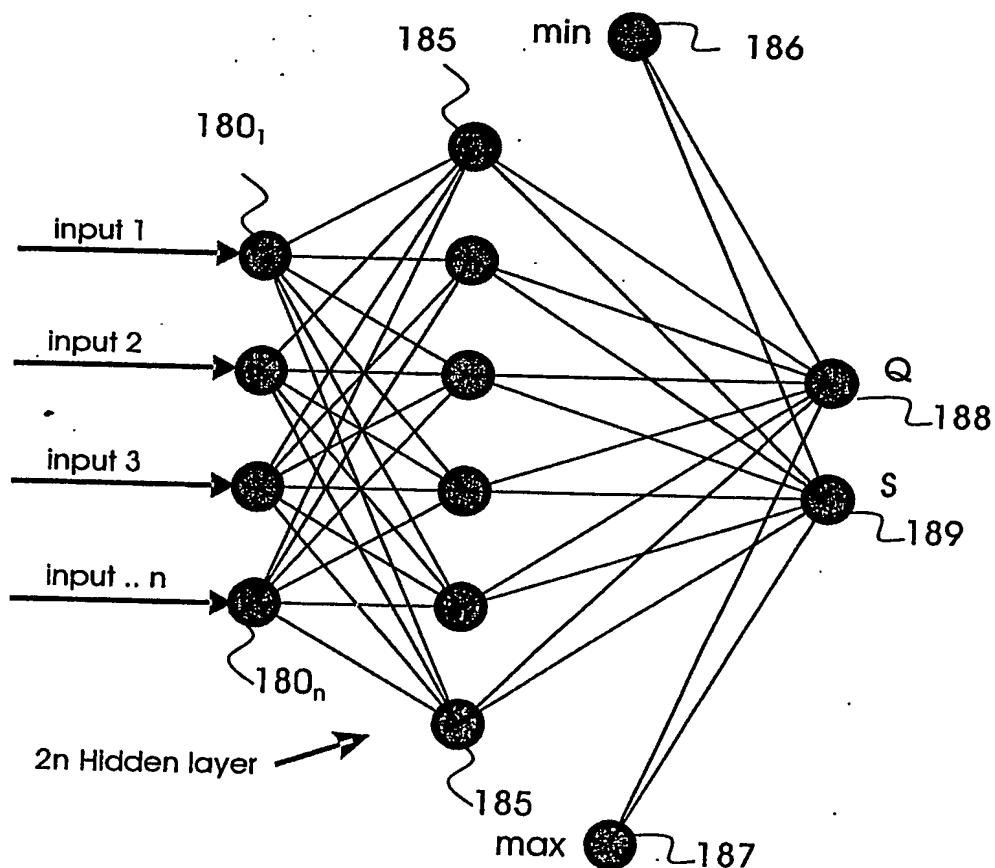
Reference image creation

Fig 17



Back propagation

Fig 18c



02/45

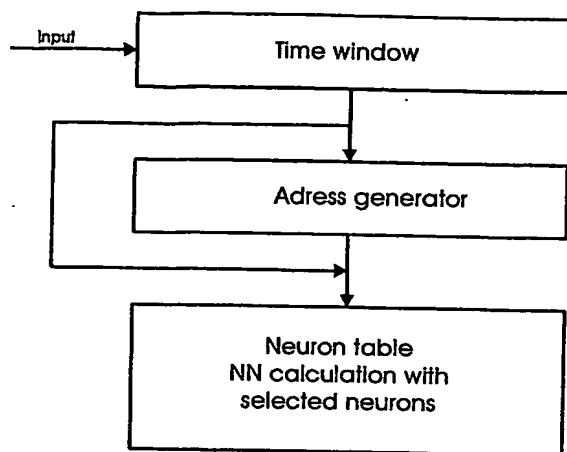


Fig 19

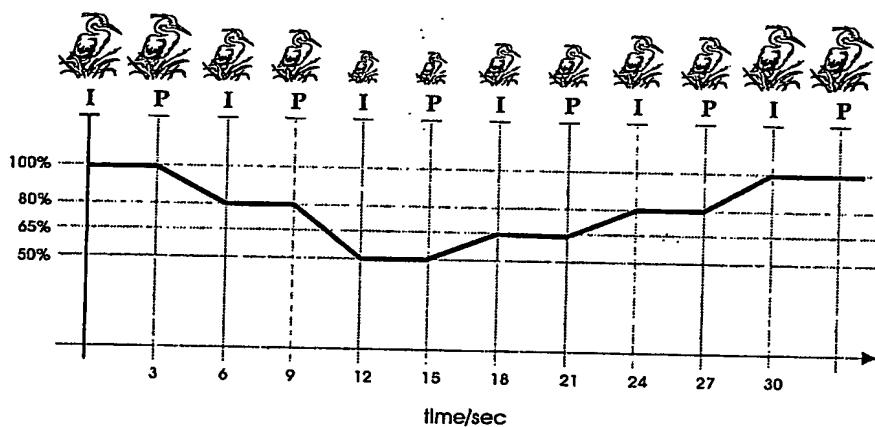


Fig 20

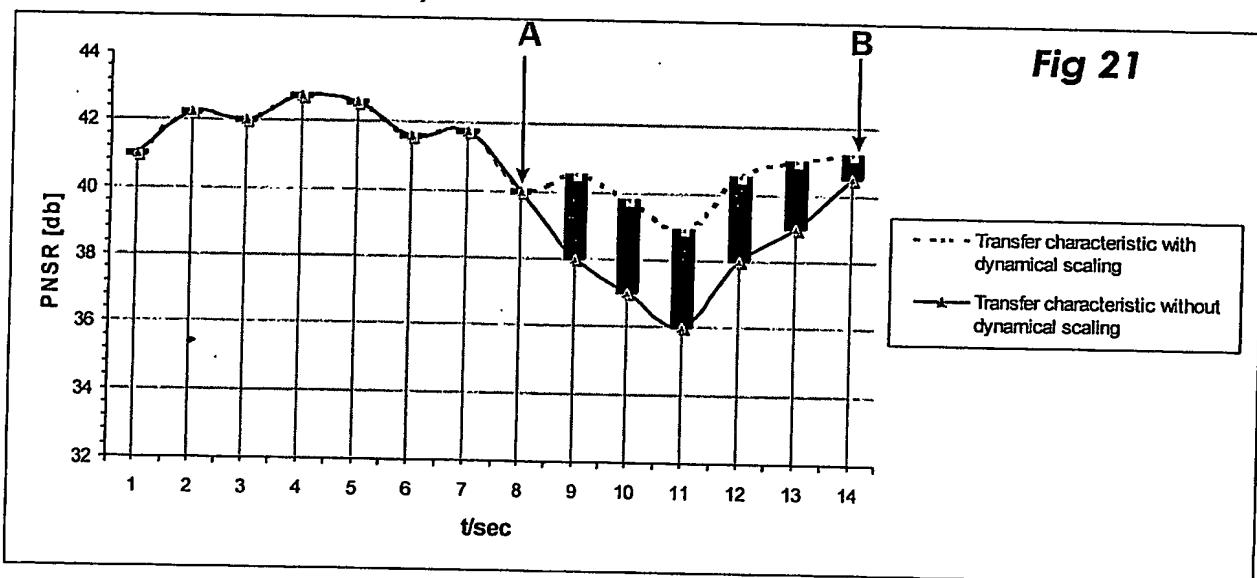


Fig 21

13/15

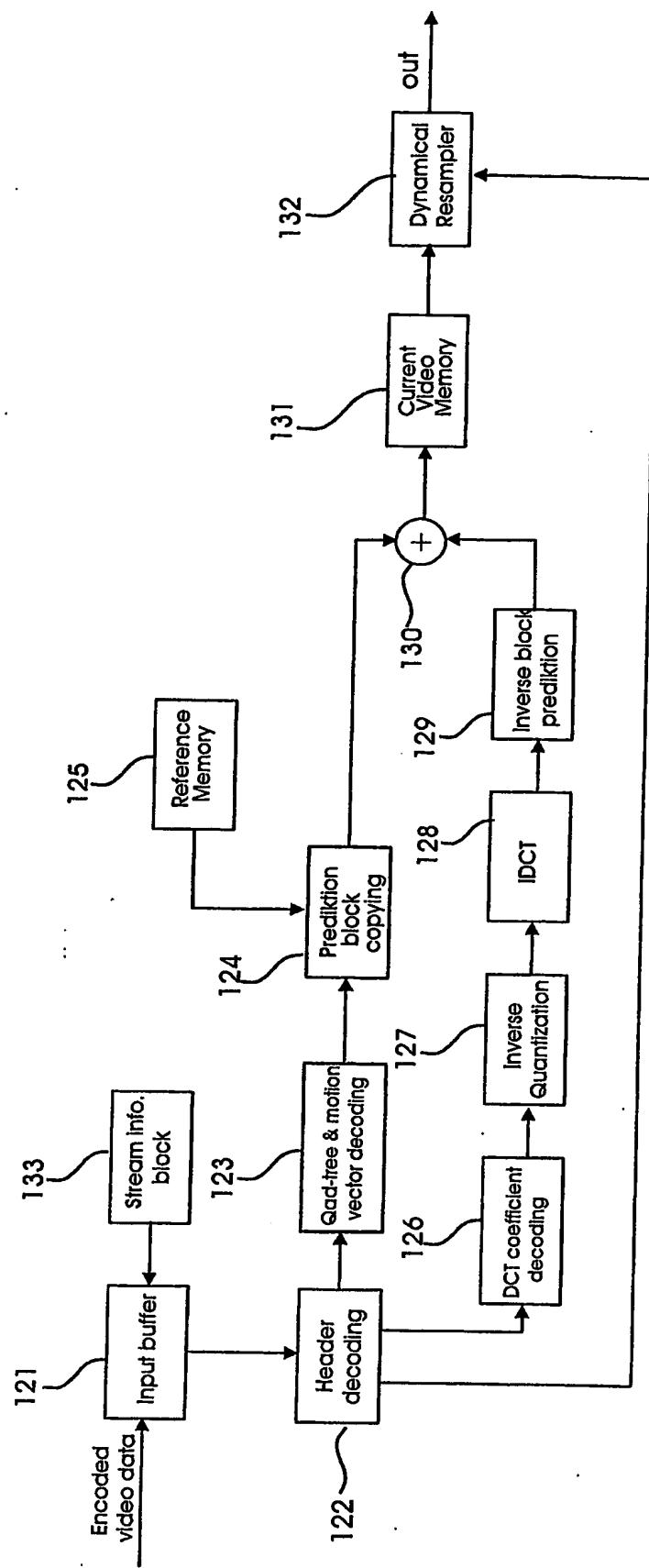
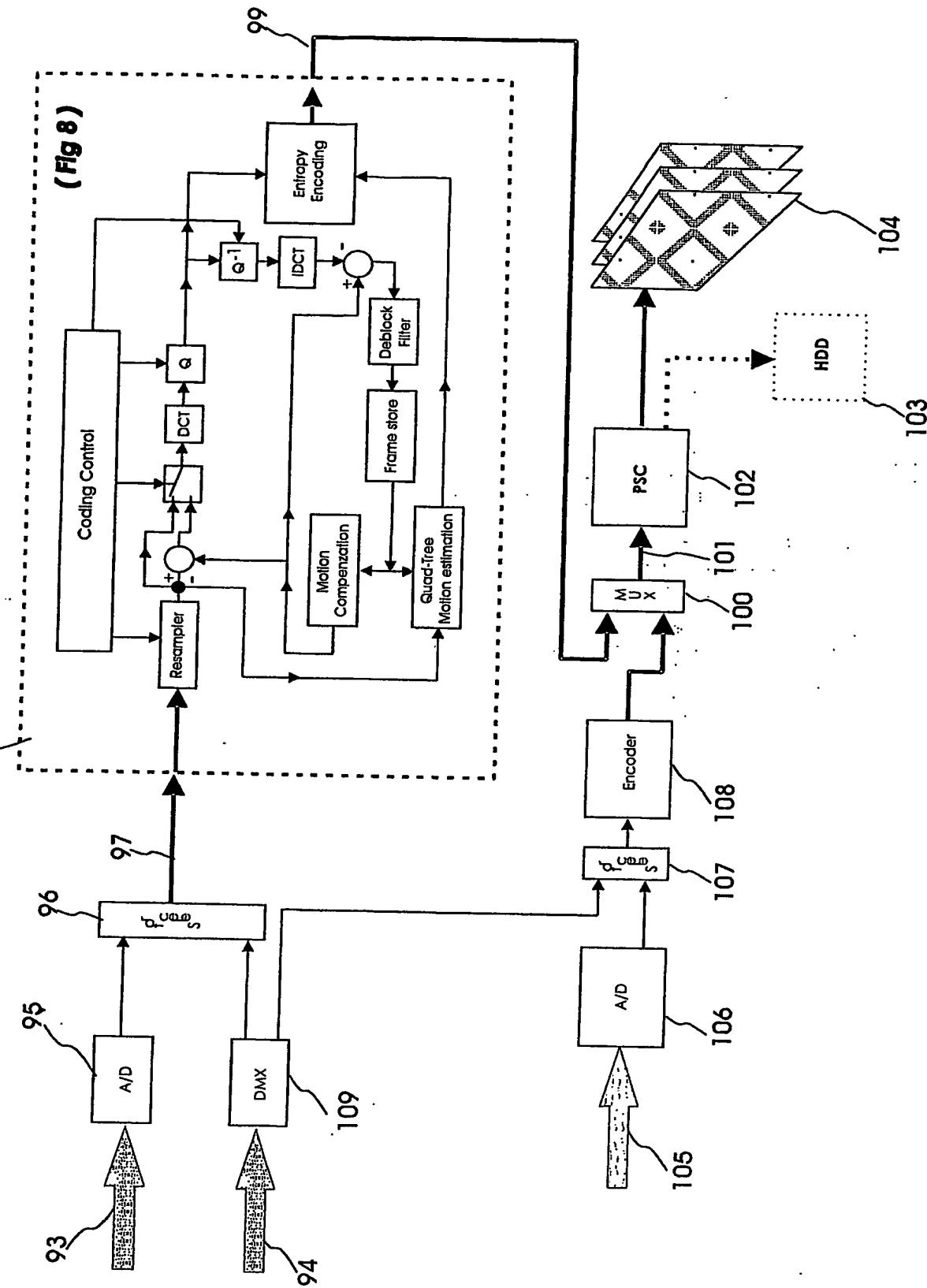


Fig. 22

415

Fig. 23



15/15

